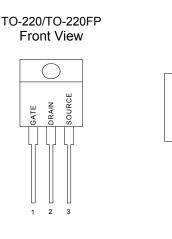


## **GENERAL DESCRIPTION**

This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

# **PIN CONFIGURATION**





2 3

1

TO-251

Front View

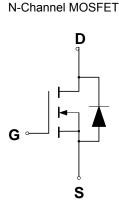
# ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current – Continuous	I <sub>D</sub>	2.0	А
<ul> <li>Pulsed</li> </ul>	I <sub>DM</sub>	4.0	
Gate-to-Source Voltage – Continue	V <sub>GS</sub>	±20	V
<ul> <li>Non-repetitive</li> </ul>	$V_{\text{GSM}}$	±40	V
Total Power Dissipation	P <sub>D(Max)</sub>		
TO-251		30	W
TO-220		83	
TO-220FP		30	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy $-$ T <sub>J</sub> = 25 $^\circ\!\mathbb{C}$	E <sub>AS</sub>	20	mJ
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 2A, L = 10mH, R_G = 25\Omega)$			
Thermal Resistance – Junction to Case	$\theta_{\text{JC}}$	1.0	°C/W
<ul> <li>Junction to Ambient</li> </ul>	θ <sub>JA</sub>	62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

## FEATURES

- Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature

### SYMBOL





## **ORDERING INFORMATION**

Part Number	Package			
CMT02N60GN251*	TO-251			
CMT02N60GN220*	TO-220			
CMT02N60GN220FP*	TO-220 Full Package			
CMT02N60XN251*	TO-251			
CMT02N60XN220*	TO-220			
CMT02N60XN220FP*	TO-220 Full Package			

\*Note: G : Suffix for Pb Free Product

X : Suffix for Halogen Free and PB Free Product

# **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J$  = 25  $^\circ\!\mathrm{C}$  .

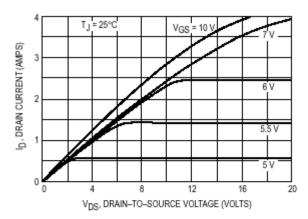
			CMT02N60			
Charac	teristic	Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	600			V	
$(V_{GS} = 0 V, I_D = 250 \mu A)$						
Drain-Source Leakage Current		I <sub>DSS</sub>				
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$					1	uA
$(V_{DS} = 480 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}$	°C)				3	
Gate-Source Leakage Current-Forw	ard	I <sub>GSSF</sub>			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Reverse		I <sub>GSSR</sub>			100	nA
$(V_{gsr} = -30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		V <sub>GS(th)</sub>	2.0	3.1	4.0	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$						
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>		3.3	4.4	Ω	
Drain-Source On-Voltage (V <sub>GS</sub> = 10 V)		V <sub>DS(on)</sub>			8.8	V
(I <sub>D</sub> = 2.0 A)						
Forward Transconductance ( $V_{DS} \ge$	50 V, I <sub>D</sub> = 1.0A) *	<b>g</b> fs	1.0			mhos
Input Capacitance	(V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,	C <sub>iss</sub>		435		pF
Output Capacitance	f = 1.0  MHz	C <sub>oss</sub>		56		pF
Reverse Transfer Capacitance	1 = 1.0 WHZ)	Crss		9.2		pF
Turn-On Delay Time	(V <sub>DD</sub> = 300 V, I <sub>D</sub> = 2.0 A,	t <sub>d(on)</sub>		12		ns
Rise Time	$V_{\rm GS} = 10  \rm V,$	tr		21		ns
Turn-Off Delay Time	ν <sub>GS</sub> = 10 ν, R <sub>G</sub> = 18Ω) *	t <sub>d(off)</sub>		30		ns
Fall Time	$R_G = 10\Omega_2$	t <sub>f</sub>		24		ns
Total Gate Charge		Qg		13		nC
Gate-Source Charge	$(V_{DS} = 400 \text{ V}, I_D = 2.0 \text{ A}, V_{GS} = 10 \text{ V})^*$	Q <sub>gs</sub>		2.0		nC
Gate-Drain Charge	$v_{GS} = 10 v$	Q <sub>gd</sub>		6.0		nC
Internal Drain Inductance		L <sub>D</sub>		4.5		nH
(Measured from the drain lead 0.2	5" from package to center of die)					
Internal Drain Inductance		Ls		7.5		nH
(Measured from the source lead 0.25" from package to source bond						
pad)						
SOURCE-DRAIN DIODE CHARAC	TERISTICS					
Forward On-Voltage(1)		V <sub>SD</sub>		1.0	1.6	V
Forward Turn-On Time	$(I_{\rm S} = 2.0 \text{ A}, V_{\rm GS} = 0 \text{ V},$	t <sub>on</sub>		75		ns
Reverse Recovery Time	$d_{IS}/d_t = 100A/\mu s)$	t <sub>rr</sub>		340		ns

\* Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%

\*\* Negligible, Dominated by circuit inductance



# **TYPICAL ELECTRICAL CHARACTERISTICS**





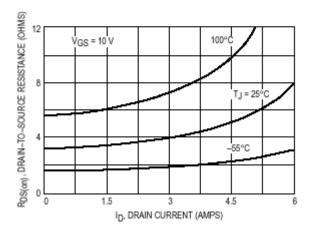


Figure 3. On–Resistance versus Drain Current and Temperature

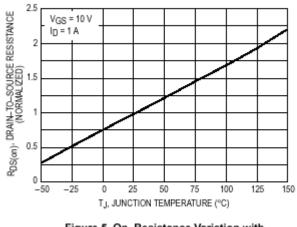


Figure 5. On–Resistance Variation with Temperature

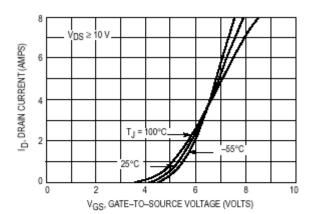


Figure 2. Transfer Characteristics

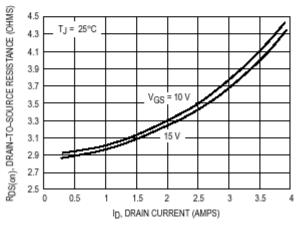
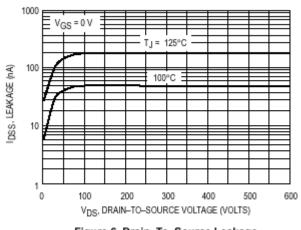
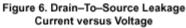


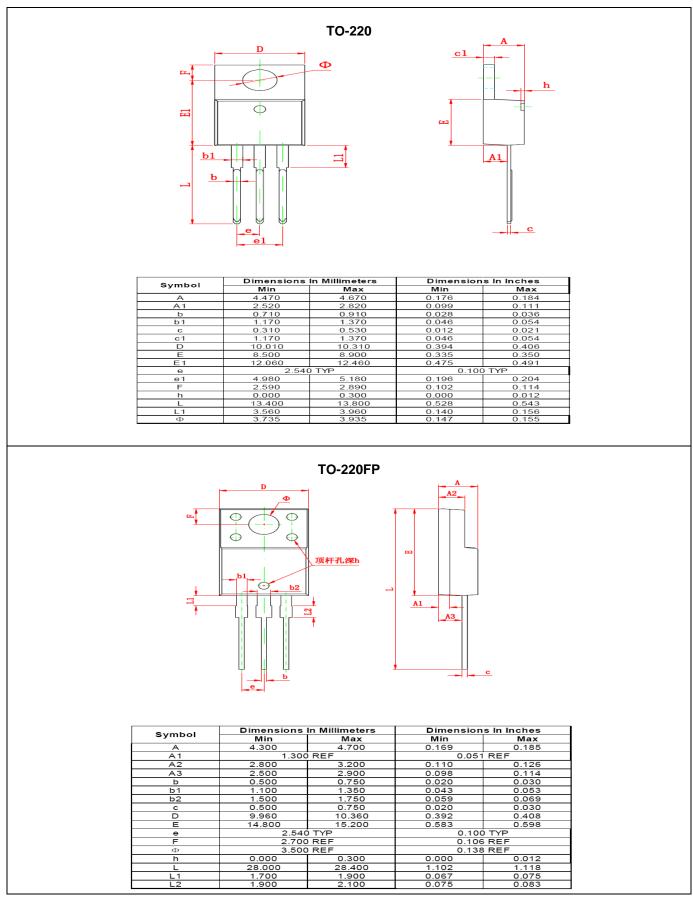
Figure 4. On–Resistance versus Drain Current and Gate Voltage





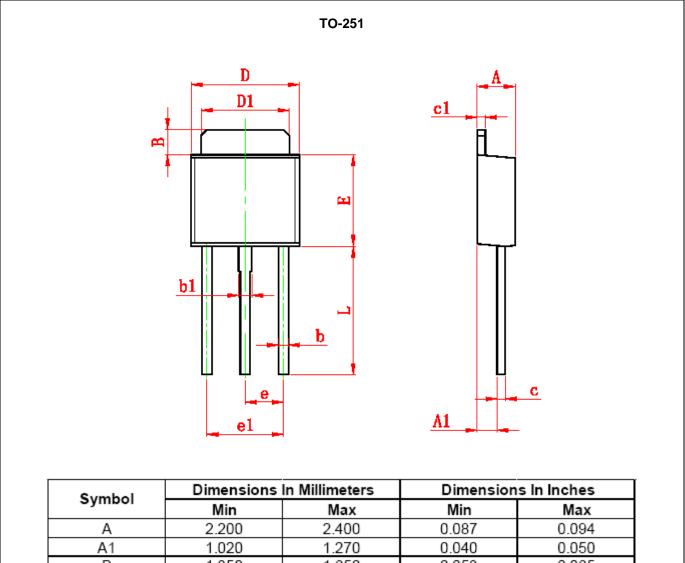


## PACKAGE DIMENSION





## PACKAGE DIMENSION



Symbol	Min	Max	Min	Max	
А	2.200	2.400	0.087	0.094	
A1	1.020	1.270	0.040	0.050	
В	1.350	1.650	0.053	0.065	
b	0.500	0.700	0.020	0.028	
b1	0.700	0.900	0.028	0.035	
с	0.430	0.580	0.017	0.023	
c1	0.430	0.580	0.017	0.023	
D	6.350	6.650	0.250	0.262	
D1	5.200	5.400	0.205	0.213	
E	5.400	5.700	0.213	0.224	
е	2.300 TYP		0.091 TYP		
e1	4.500	4.700	0.177	0.185	
L	7.500	7.900	0.295	0.311	



# **IMPORTANT NOTICE**

Champion Microelectronic Corporation (CMC) reserves the right to make changes to its products or to discontinue any integrated circuit product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

A few applications using integrated circuit products may involve potential risks of death, personal injury, or severe property or environmental damage. CMC integrated circuit products are not designed, intended, authorized, or warranted to be suitable for use in life-support applications, devices or systems or other critical applications. Use of CMC products in such applications is understood to be fully at the risk of the customer. In order to minimize risks associated with the customer's applications, the customer should provide adequate design and operating safeguards.

#### HsinChu Headquarter

### 5F, No. 11, Park Avenue II, Science-Based Industrial Park, HsinChu City, Taiwan TEL: +886-3-567 9979 FAX: +886-3-567 9909

#### Sales & Marketing

21F., No. 96, Sec. 1, Sintai 5th Rd., Sijhih City, Taipei County 22102, Taiwan, R.O.C. TEL: +886-2-2696 3558 FAX: +886-2-2696 3559